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GATE TURN-OFF THYRISTOR

ATG677

Repetitive voltage up to

Mean on-state current

Controllable on-state current

Surge on-state current

4500 V

685 A

2000 A

13 kA

FINAL SPECIFICATION

gen 19 - ISSUE: 01

Symbol	Characteristic	Conditions	Tj	Value			Unit
			°C	min	typ	max	
BLOCK						1	
V DRM	Repetitive peak off-state voltage					4500	V
V RRM	Repetitive peak reverse voltage					17	V
I DRM	Repetitive peak off-state current	V _D =VDRM R _{GK} <2 ohm	125			150	mA
I RRM	Repetitive peak reverse current	V _R =VRRM				10	mA
(dv/dt) crit	Critical rate of rise of off-state voltage, min	Linear ramp up to 50% VDRM, shorted G-K				1000	V/uS
CONDU							
I T (AV)	Mean on-state current	180° sin, 50 Hz,Th=70°C, double side cooled				685	Α
I TSM	Surge on-state current	sine wave, 10 ms, no reverse voltage	125			13	kA
l² t	I ² t for fusing coordination	10ms, no reverse voltage				845	A ² s10 ³
Vт	On-state voltage	On-state current = 2000 A	125			3,6	V
V T(TO)	Threshold voltage		125			1,80	V
rт	On-state slope resistance					0,90	mohm
SWITC	HING ON						
t gt	Gate controlled turn on time	$I_T = 2000A$; di/dt = 200A/uS	125			10	uS
t d	Delay time	$I_{GM} = 30A$; $di_{GR}/dt = 20A/uS$					uS
E on	Turn-on switching energy	V _D = 2500V					J
(di/dt) crit	Critical rate of rise of on-state current	$I_T = 2000A$, $I_{GM} = 30A$, $di_{GR}/dt = 20A/uS$	125			400	A/uS
,	HING OFF						
I _{TCM}	Controllable peak on-state current					2000	Α
t gq	Gate controlled turn-off time	$I_{TC} = I_{TCM}, V_{DM} = 3400V$				25	uS
99	Storage time	$I_{GM} = 20 \text{ A di}_{GR}/\text{dt} = 35\text{A/uS}$	125				uS
_	Turn-off switching energy	$L_{\rm S} = 0.3 \mu{\rm H}$; $C_{\rm S} = 4 {\rm uF}$, $R_{\rm S} = 5 {\rm ohm}$					J
1	Peak Turn-off reverse gate current					800	A
I RG	Spike voltage	_				000	V
V _{DSP} TRIGGI	-						V
V GT		V _D =24V	25			1.5	V
	Gate trigger voltage	V _D =24V	25			1,5	-
I GT	Gate trigger current					2,5	A V
V GRM	Peak reverse gate voltage	$V_{RG} = V_{RGM}$	25 125			17 10	mA
DISSIP	Peak reverse leakage gate current	VRG = VRGM	123			10	IIIA
	Thermal resistance junction to heatsink d.c.	Double side cooled				22	°C/kW
R _{th(j-h)}	Virtual junction temperature	Double side cooled				125	°C
T _{Vj} Teta	Storage temperature			-40		150	°C
sig				-40		130	
MOUN	IING					ı	
W	Weight				800		g
F	Mounting force			17.0	/	24.0	kN

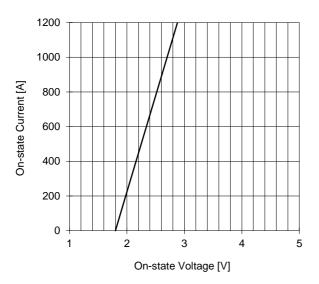
standard specification ____

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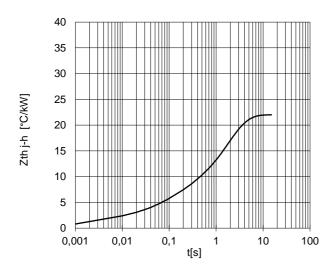


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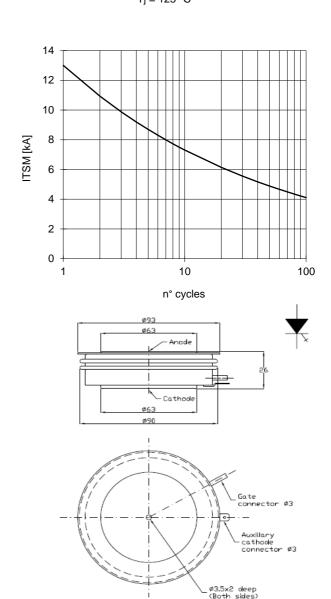
ON-STATE CHARACTERISTIC Tj = 125 °C



TRANSIENT THERMAL IMPEDANCE DOUBLE SIDE COOLED



SURGE CHARACTERISTIC Tj = 125 °C



All the characteristics given in this data sheet are guaranteed only with uniform clamping force, cleaned and lubricated heatsink, surfaces with flatness < .03 mm and roughness < 2 μ m.

In the interest of product improvement POSEICO Spa reserves the right to change any data given in this data sheet at any time without previous notice.

If not stated otherwise the maximum value of ratings (simbols over shaded background) and characteristics is reported.

Distributed by

Dimensions in mm