

## PHASE CONTROL THYRISTOR

# AT975

Repetitive voltage up to **5200 V**  
Mean on-state current **3965 A**  
Surge current **60 kA**

### FINAL SPECIFICATION

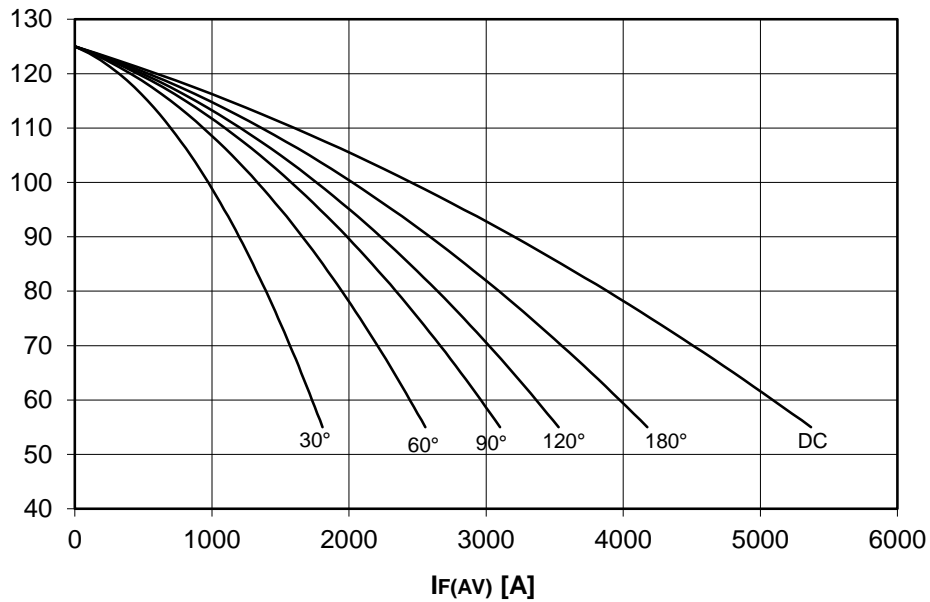
July 22 - Issue: 4

Symbol	Characteristic	Conditions	T <sub>j</sub> [°C]	Value	Unit
<b>BLOCKING</b>					
V <sub>RRM</sub>	Repetitive peak reverse voltage		125	5200	V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage		125	5300	V
V <sub>DRM</sub>	Repetitive peak off-state voltage		125	5200	V
I <sub>RRM</sub>	Repetitive peak reverse current	V=VRRM	125	300	mA
I <sub>DRM</sub>	Repetitive peak off-state current	V=VDRM	125	300	mA
<b>CONDUCTING</b>					
I <sub>T(AV)</sub>	Mean on-state current	180° sin, 50 Hz, T <sub>c</sub> =70°C, double side cooled		3965	A
I <sub>TSM</sub>	Surge on-state current	sine wave, 10 ms	125	60.0	kA
I <sup>2</sup> t	I <sup>2</sup> t	without reverse voltage		18000 x1E3	A <sup>2</sup> s
V <sub>T</sub>	On-state voltage	On-state current = 3000 A	25	1.50	V
V <sub>T(TH)</sub>	Threshold voltage		125	1.04	V
r <sub>T</sub>	On-state slope resistance		125	0.130	mohm
<b>SWITCHING</b>					
di/dt	Critical rate of rise of on-state current, min.	From 67% VDRM, gate 10V 5ohm	125	200	A/μs
dv/dt	Critical rate of rise of off-state voltage, min.	Linear ramp up to 67% of VDRM	125	1000	V/μs
t <sub>d</sub>	Gate controlled delay time, typical	VD=0.4 VRM, IGM=2 A	25	3	μs
t <sub>q</sub>	Circuit commutated turn-off time, typical	dv/dt = 20 V/μs linear up to 75% VDRM		400	μs
Q <sub>RR</sub>	Reverse recovery charge		125	4500	μC
I <sub>RR</sub>	Peak reverse recovery current	IT=2100 A, di/dt = 3 A/μs, VR=100 V		150	A
t <sub>RR</sub>	Reverse Recovery Time			60	μs
I <sub>H</sub>	Holding current, typical	VD=5V, gate open circuit	25	500	mA
I <sub>L</sub>	Latching current, typical	VD=12V, tp=50μs	25	1500	mA
<b>GATE</b>					
V <sub>GT</sub>	Gate trigger voltage	VD=12V	25	2.5	V
I <sub>GT</sub>	Gate trigger current	VD=12V	25	250	mA
V <sub>GD</sub>	Non-trigger gate voltage, min.	VD=67%VDRM	125	0.25	V
V <sub>FGM</sub>	Peak gate voltage (forward)			10	V
I <sub>FGM</sub>	Peak gate current			10	A
V <sub>RGM</sub>	Peak gate voltage (reverse)			10	V
P <sub>GM</sub>	Peak gate power dissipation	Pulse width 100 μs		150	W
P <sub>G</sub>	Average gate power dissipation			3	W
<b>MOUNTING</b>					
R <sub>th(j-c)</sub>	Thermal impedance, DC	Junction to case, double side cooled		6.0	°C/kW
R <sub>th(c-h)</sub>	Thermal impedance	Case to heatsink, double side cooled		1.5	°C/kW
T <sub>j</sub>	Operating junction temperature			-30 / 125	°C
F	Mounting force			80.0 / 100.0	kN
	Mass			3000	g
<b>ORDERING INFORMATION : AT975 S 52</b> standard specification <input type="checkbox"/> <input type="checkbox"/> VDRM&VRRM/100					

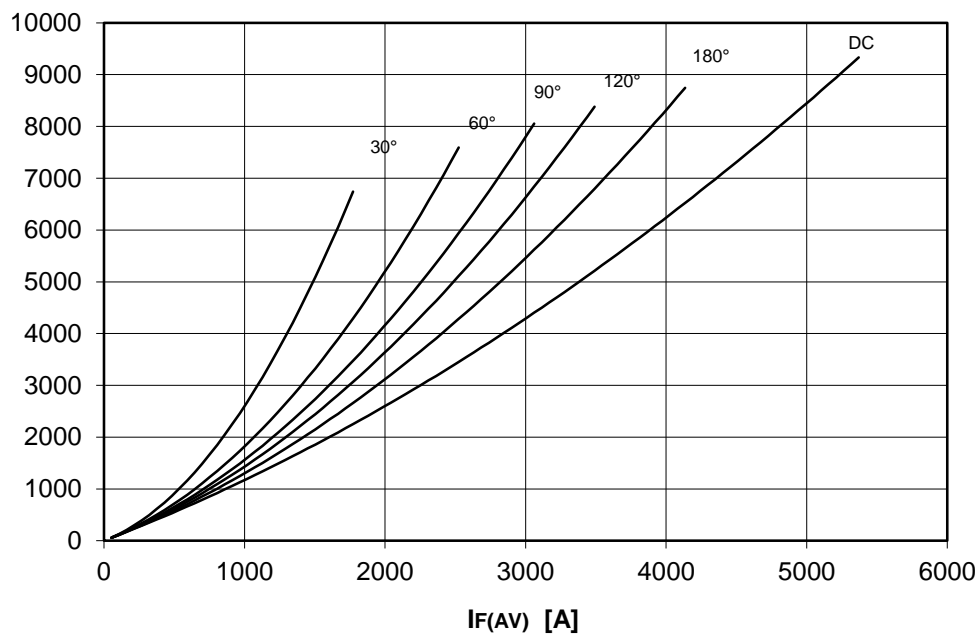
DISSIPATION CHARACTERISTICS

SQUARE WAVE

Th [°C]



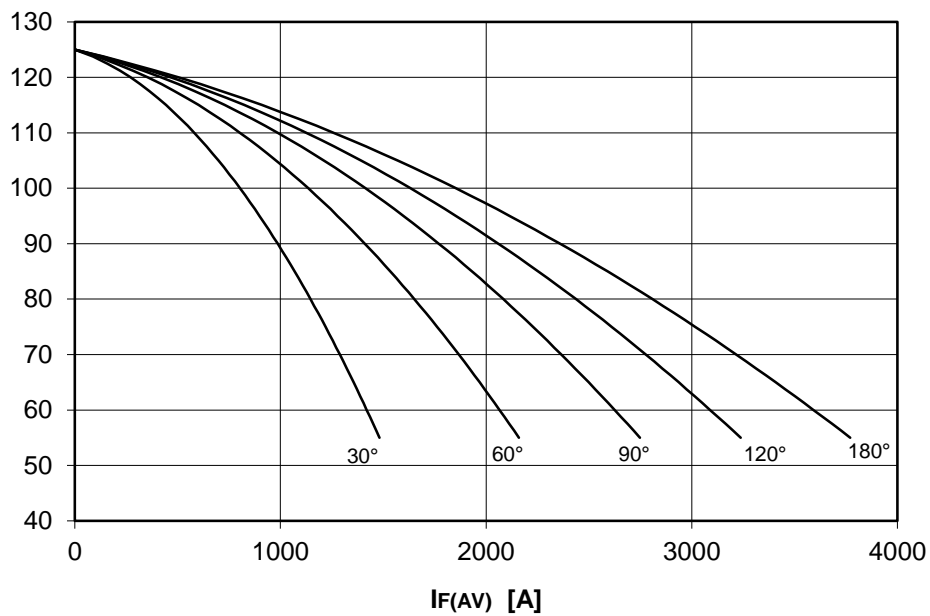
PF(AV) [W]



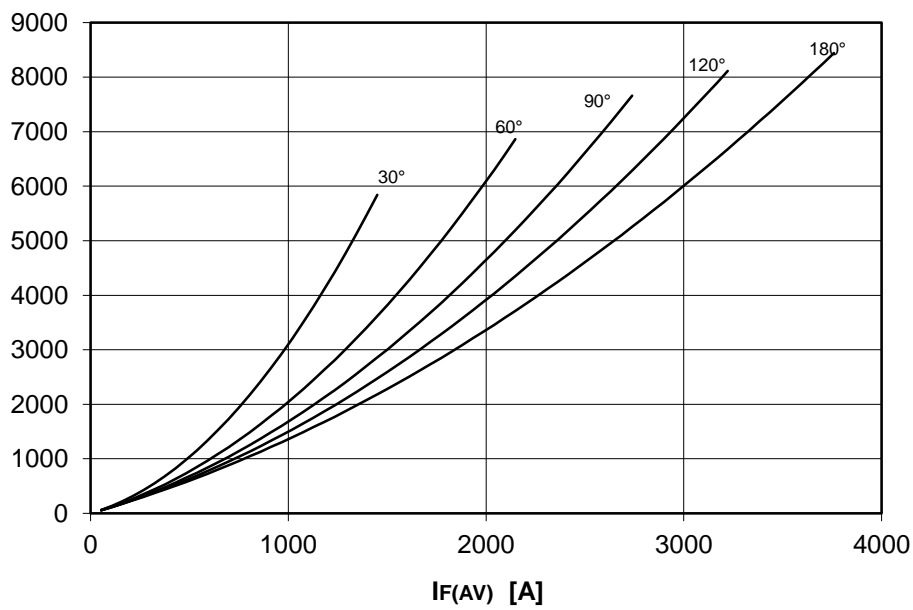
DISSIPATION CHARACTERISTICS

SINE WAVE

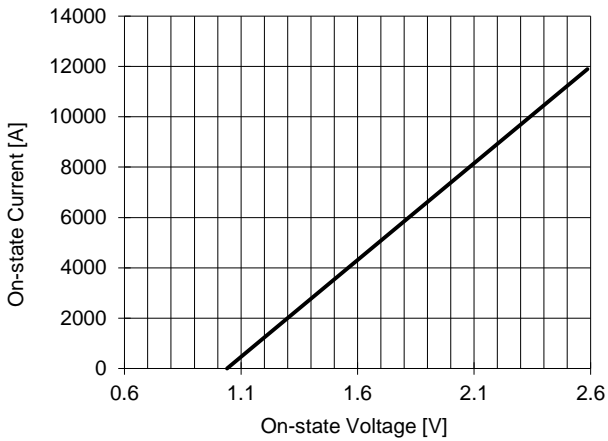
Th [°C]



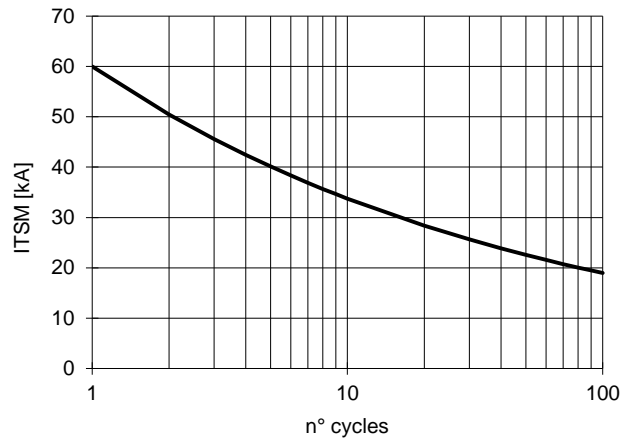
PF(AV) [W]



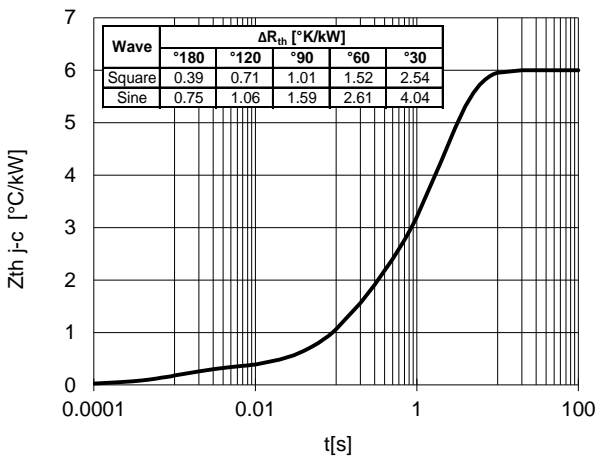
ON-STATE CHARACTERISTIC  
T<sub>j</sub> = 125 °C



SURGE CHARACTERISTIC  
T<sub>j</sub> = 125 °C

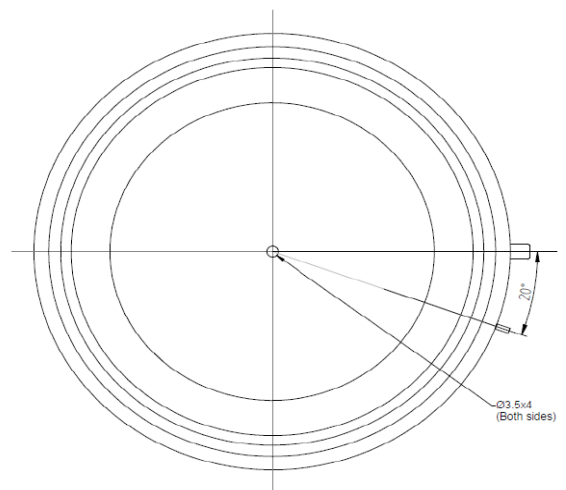
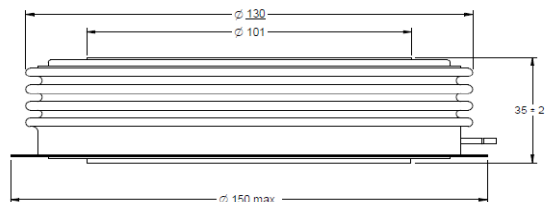


TRANSIENT THERMAL IMPEDANCE  
DOUBLE SIDE COOLED



$$Z_{th\ j-c}(t) = \sum_{i=1}^n A_i * \left(1 - e^{-\frac{t}{\tau_i}}\right)$$

i	1	2	3	4
A <sub>i</sub> [°C/kW]	2.738	1.779	1.186	0.297
τ <sub>i</sub> [s]	2.4	1.70	0.16	0.001



Dimensions in mm



Cathode terminal type DIN 46244 - A 4.8 - 0.8

Gate terminal type AMP 60598 - 1

All the characteristics given in this data sheet are guaranteed only with uniform clamping force, cleaned and lubricated heatsink, surfaces with flatness < .03 mm and roughness < 2 μm.

In the interest of product improvement POSEICO SpA reserves the right to change any data given in this data sheet at any time without previous notice.

If not stated otherwise the maximum value of ratings (symbols over shaded background) and characteristics is reported.

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